

CURRICULUM VITAE

M.Venkateswara Reddy,
S/O M.Prasad Reddy,
Tukkuluru(post),Nuzvid(Mandal),
Krishna District,
Andhra Pradesh.

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CAREER OBJECTIVE

To work in challenging environment as an Electrical Engineer. Utilizing my knowledge to my best in providing solution in diverse domains and also where i can upgrade my skills all the time according to the global market.

ACADEMIC CREDENTIALS

- Pursuing Ph.D, Osmania University, Hyderabad
- Master of Engineering(M.E) specializing in Power Electronics and Drives, Jerusalem College of Engineering, Anna University, Chennai, 2006
- Bachelor of Engineering (B. E) specializing in Electrical & Electronics Engineering,Thangavalu Engineering College, University of Madras, Chennai, 2003
- Diploma in Mining Engineering, Govt. Polytechnic College, Kothagudem, 1997

IT SKILLS & PROFICIENCY

Programming Languages	:	C, C++
Simulation Software's	:	ETAP,PV-Syst , P-Spice, MAT LAB, PSIM, PSAF, EMTP-RV
Operating Systems	:	Windows10, 95/98/XP,
Application Packages	:	MS Office
Other Skills	:	Protection schemes for Power Systems, Electrical Drives

CERTIFICATION

- Completed Certification in **Introduction to Renewable Energy** course,
Solar Training and Renewable Energy Education, Carbondale with **1003.33 0.00-1120.00 89.58 %**

CAREER SUMMARY

- ◆ I have worked as sub engineer (trainee) in an underground and open cast coal mine nearer to Kothagudem, Andhra Pradesh from July 1997to June 1999
- ◆ I have worked as Asst. Professor in EEE dept. at **Mother Teresa Institute of Science and Technology, Sathupally, Khammam**, from July 2005 to 27.sep.2007

- ◆ I have worked as H.O.D OF EEE DEPT at **Mother Teresa Institute of Science and Technology, Sathupally, Khammam** from 27.09.2007 to 13.08.2008
- ◆ I have worked as H.O.D OF EEE DEPT at **Sri Rama Institute of Technology & science kupenakuntala, penuballi, Khammam**, from Aug 2008 to Jan 2009
- ◆ I have worked as an Assoc.Prof in EEE Dept. at **Sai Spurthi Institute of Technology, B.gangaram, Sathupally, Khammam** from Jan 2009 to May2011
- ◆ I have worked as an Assoc.Prof & HOD in EEE Dept. at **Vikas college of Engg. and technology , Nunna, Vijayawada, Krishna dist Andhra Pradesh** from June 2011 to may 2015
- ◆ I have worked as an Assoc.Prof in EEE Dept. at **RRS College of engineering andtechnology, PATAN CHERUVU, Hyderabad**, from June 2015 to Jan 2016
- ◆ Presently working as Asst.Prof in EEE Dept. at **Gokhale Education Society's R. H. Sapat College of Engineering, Management Studies and Research, Nashik**. From Jan 2016

ACADEMIC PROJECT WORK

Academic project 1 (D.MN.E)

Title: long wall mining

This project deals with the different types of extraction of coal in long wall by using different long wall coal machines & their operation

Academic project 2 (B.E)

Title: Power system protection

This project deals with protection of Turbo Alternators, bus bars, and transformers by using different circuit breakers from internal & external faults.

Project 3 (M.E)

Title: A low frequency to high frequency AC- AC inverter with soft switching

This project deals with a full bridge ac-ac inverter for high frequency power distribution system controlled by a unified controller.

Presently working Project 4 (Ph.D)

Title: Inter line power flow controller

This project deals with design & implementation of inter line power flow controller for transferring power from over loaded line to under loaded line

POWER ELECTRONIC DESIGN SUMMARY

- ❖ •High power inverter designs (1KW – 100KW) using IGBT devices, very strong design knowledge of inverters including gate drive design, low inductance bus design, and high power snubber design.
- ❖ Exhibit a superb background in power electronic device knowledge and design, with heavy focus on IGBT, MOSFET, and SCR technologies.
- ❖ Understand the effects of PWM waveforms on motor and transformer insulation systems.
- ❖ Power supply design (both linear and switching), using many of the standard switching topologies.

Research & development:

- ❖ worked as *Project Coordinator and in charge of R&D DEPT* at **Sai Spurthi Institute Of Technology, Sathupally** handled various projects related to power systems, designing of Power Electronics products using MOSFETs, IGBTs, GTOs, SCRs etc.
- ❖ Worked as a *Project Coordinator and in charge of R&D DEPT* at **Mother Teresa Institute of Science and Technology, Sathupally**.
- ❖ Worked as *Project Co-coordinator and in charge R&D DEPT* in **Vikas College of Engg. and technology**.
- ❖ Expertise in selecting the right devices / components while designing projects in Power systems and power electronics (Simulation/Hardware Models). Software's like ETAP, PSICE, MATLAB, PSIM, EMTP _RV, B2SPICE.

Special Achievements & Awards

I have worked as chief superintendent for DIPLOMA (APSBTET) board examinations I have worked as reviewer for power, control, and communication and computational technologies for sustainable growth., Conference conducted by G.Pullareddy College of engineering, Kurnool (<http://pccctsg-conf.org>)

I am working as reviewer for international journal of power electronics drive systems

I am working as Assoc. Editor for Journal of Energy Harvesting and Storage

I am working as Assoc. Editor for Journal of Education and Learning

I am working as editorial advisory member for international journal of advanced technology in engineering and science (<http://www.ijates.com/editorboard.html>)

- I have attended one week work shop on **RECENT TRENDS ON HIGH VOLTAGE TESTING**, conducted by **Indian Institute of Technology, Madras**.
- I have attended a two-day course on “**RECENT TRENDS ON HIGH VOLTAGE TESTING**”, conducted by Saradhi Institute of Technology, Nuzvid.

- I have attended a two-day workshop on “RECENT **TRENDS ON HIGH VOLTAGE TESTING**”, conducted by Usha Rama College of Engineering And Technology, TERLAPROLU.
- I have attended One Week Online National Level Faculty Development Programme (To help minimize the impact of the COVID-19 outbreak on Faculty.) July 5, 2021 to July 9, 2021. “Advances in SCADA & 5 G Technology organized by Sir Visvesvaraya Institute of Technology, Nashik.
- I have attended One Week National Level Faculty Development Program (FDP) On “Essentials of Data Science” from 27th July - 31st July 2021 organized by Gokhale Education Society’ R H Sapat College of Engineering, Management Studies & Research.
- I have attended online FDP online "Simulation Software for Electrical Engineers” held from 8/02/2021 to 12/02/2021 at “Swami Keshvanand Institute of Technology, Management & Gramothan, Jaipur”.
- I have attended online FDP “Current Trends in Solar and Wind Technologies" held from 16/01/2021 to 20/01/2021 at "Poornima College of Engineering".
- I have attended online FDP Five Day faculty development program on Renewable Energy Technologies held from 26/02/2021 to 02/03/2021 at Birla Institute of Engineering & Technology, Pilani (Rajasthan)
- I have attended One Week Online National Level Faculty Development Programme on power quality issues in smart grid conducted by Ajay Kumar Garg Engineering College, Ghaziabad.
- Published a paper entitled, Comparison of performance of FACTS device – Inter Line Power Flow Controller (IPFC) and Power System Stabilizer (PSS) in IEEE 9 Bus System at Jour of Adv Research in Dynamical & Control Systems, Vol. 10, 02-Regular Issue, 2018
- Published a paper entitled, Design of Multi Level Converter for Interline Power Flow Controller with Variable Inputs in SIMULINK/MATLAB at International Journal of Emerging Technology and Advanced Engineering
- Published a paper entitled, Design of Controller for an Interline Power Flow Controller and Simulation in MATLAB at PEIE 2011, CCIS 148, pp. 56–61, 2011. © Springer-Verlag Berlin Heidelberg 2011
- Published a paper entitled, Design of Wind Solar Hydro Hybrid System fed IEEE 9 Bus System and enhancing the Performance of IPFC under abnormal climatic conditions using MATLAB/ Simulink.
- Published a paper entitled, Enhancement of Transient Stability in Fourteen Bus system using Interline Power Flow Controller, Proceedings of the IEEE 2017 International Conference on Computing Methodologies and Communication, (ICCMC).
- Published a paper entitled Enhancement of Voltage Profile for IEEE 14 Bus System with Inter line Power Flow Controller, 2016 Biennial International Conference on Power and Energy Systems: Towards Sustainable Energy (PESTSE)

- I have presented a paper title on " low frequency to high frequency ac-ac inverter" in national conference conducted by Bharat Institute Of Higher Education And Research, Chennai.
- I have paper presented in national conference title on" new single phase near unity power factor pwm converter / inverter fed 3 phase induction motor on 2,3-4-2010 in Valliammai Engg. College Kattankulathur, Chennai.
- I have published a paper on transformer less dc – dc converter in international journal of engineering trends and technology- volume2 issue 3- 2011 by IJETT
- Holding Life Membership from ISTE Organization.

PERSONAL PROFILE

Name : Mallireddy Venkateswara Reddy
 Father's Name : Mallireddy Prasad Reddy
 Sex : Male
 Languages known : Telugu, Hindi, English, Tamil & Marathi
 Permanent Address : M. Venkateswara reddy,
 Tukkuluru (Post),
 Nuzvid (M.D),
 Krishna (dist).
 Andhrapradesh
 India
 521201

Hobbies & STRENGTHS

- Gardening, listening music, Cricket and Cooking.
- Efficient time management and responsibilities handling capability.
- Good at working efficiently in a group as well as an individual.
- Leadership qualities, confident with better communications skills.

DECLARATION

I hereby affirm that the above information is true to the best of my knowledge

Date:

Place:

M. Venkateswara Reddy